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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/530,605	03/11/2008	Jean-Francois Peyre	GEI-107	9052
47888 7590 10/04/2010 HEDMAN & COSTIGAN, P.C. 1230 AVENUE OF THE AMERICAS 7th floor NEW YORK, NY 10020				
EXAMINER				
CHOW, YUK				
ART UNIT		PAPER NUMBER		
2629				
MAIL DATE		DELIVERY MODE		
10/04/2010		PAPER		

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

### Office Action Summary

**Application No.**

10/530,605

**Applicant(s)**

PEYRE ET AL.

**Examiner**

YUK CHOW

**Art Unit**

2629

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-10 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-10 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 07 April 2005 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO/SB/CD)  
Paper No(s)/Mail Date 04/07/2005
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date: \_\_\_\_
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: \_\_\_\_

## DETAILED ACTION

### *Drawings*

1. The subject matter of this application admits of illustration by a drawing to facilitate understanding of the invention. Applicant is required to furnish a drawing under 37 CFR 1.81(c). No new matter may be introduced in the required drawing. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d).

Figures 3-6 are missing in the drawings.

Figure 2 contains inconsistent sub-figure labeling.

2. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the **"electronic microcircuit"** must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate

changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

***Claim Rejections - 35 USC § 102***

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1-3, 5-7 and 9-10 are rejected under 35 U.S.C. 102(b) as being anticipated by Nishida (US 5,767,818).

As to **claim 1**, Nishida discloses a display device comprising individual elements wherein the placement, on each element, of an electronic microcircuit (Fig. 9(85, 86 and 87)) capable of recognizing its address among those of other microcircuits connected in parallel to the common addressing electrodes (see Fig. 8 and 9, 86 is connected in parallel to address electrode 84A), recording the brightness data sent to its address and modulating the brightness of its display element according to the data received (see Col. 2 lines 31-43).

As to **claim 2**, Nishida discloses the device of claim 1, wherein it includes one display element and one microcircuit per elementary color point of the image (Fig. 9, one microcircuit 85 per elementary color).

As to **claim 3**, Nishida discloses the device of claim 1, wherein each microcircuit manages several color points or elementary points adjacent to the image on one or more addresses (Fig. 9(86) manages several color points adjacent to the image, see Col 13 lines 8-20).

As to **claim 5**, Nishida discloses the device of claim 1, wherein the individual addresses are written electrically in a non-volatile memory area of the microcircuit during assembly of the display screen (see Col. 11 lines 10-37).

As to **claim 6**, Nishida discloses the device of claim 1, wherein the display screen is divided into several addressing areas controlled independently and simultaneously so as to reduce the frequency of the addressing signals (see Fig. 10, display screen is divided into several areas, and Fig. 12, 13 show address assignment).

As to **claim 7**, Nishida discloses the device of claim 1, wherein each microcircuit can record, in addition to its individual address and in a preset order, the addresses of one or more display elements, adjacent or otherwise, and is capable, if it detects the transmission of one of said recorded addresses, of counting the number of brightness data sent in series and without a new address, and loading after this counting the data which is addressed to it (see Col. 10 line 40-61).

As to **claim 9**, Nishida discloses the device of claim 1, wherein the microcircuit has means enabling it to correct the modulation (see Fig. 9(88)) of its display element

according to a local measurement, for example of the current, or a correction coefficient sent by the addressing system (see Col. 9 lines 18-53), .

As to **claim 10**, Nishida discloses the device of claim 1, wherein the microcircuit has means enabling it to recognize certain preset global addresses and then to place itself in a preset test mode (see Col. 10 line 34-Col. 11 line 10).

### ***Claim Rejections - 35 USC § 103***

5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

6. Claim 4 is rejected under 35 U.S.C. 103(a) as being unpatentable over Nishida (US 5,767,818) in view of Katsu et al.(US 6,597,427).

As to **claim 4**, Nishida discloses the device of claim 1.

However, Nishida's disclosure does not teach the individual addresses are engraved on a microcircuit during its manufacture and remain visible for optical reading of this said address during assembly of the display screen.

Katsu discloses a LCD identification detection method wherein teaches individual address is marked on circuitry in the pixel region (see Fig. 4 (50)).

It would have been obvious for one ordinary skill in the art at the time invention was made to incorporate address information identification marking as in Katsu into

display device of Nishida, because it's easy to locate the malfunctioning device and precision of the position detection can be increased (see Katsu Col. 10 lines 27-45).

7. Claim 8 is rejected under 35 U.S.C. 103(a) as being unpatentable over Nishida (US 5,767,818) in view of Edwards (US 5,194,862).

As to **claim 8**, Nishida discloses the device of claim 1.

However, Nishida's disclosure does not teach the microcircuit has means enabling it to detect that the user has touched or pressed the screen close to the corresponding display element and means to send this information back, with its individual address, to the common addressing and power supply electrodes.

Edwards discloses a touch sensor array system and display system wherein teaches an array of addressed sensing elements (Fig. 1(12)) and addressable through operation of switching device by the addressing means.

It would have been obvious for one ordinary skill in the art at the time invention was made to incorporate the touch sensor as in Edwards into display device of Nishida, because such active addressing of the sensing elements enables large numbers of sensing elements to be used in the array thereby providing a high element density or high resolution and without the kind of problems which could be expected when using a simple multiplexing technique for monitoring the sensing element states (see Edwards Col. 1 line 54-Col. 2 line 12).

***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to YUK CHOW whose telephone number is (571)270-1544. The examiner can normally be reached on 8-6 M-TH E.T..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Quan-Zhen Wang can be reached on (571) 272-3114. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Y. C./  
Examiner, Art Unit 2629

/Quan-Zhen Wang/  
Supervisory Patent Examiner, Art Unit 2629